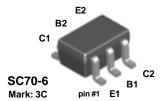


BC857S



NOTE: The pinouts are symmetrical; pin 1 and pin 4 are interchangeable. Units inside the carrier can be of either orientation and will not affect the functionality of the device.

PNP Multi-Chip General Purpose Amplifier

This device is designed for general purpose amplifier applications at collector currents to 200 mA. Sourced from Process 68.

Absolute Maximum Ratings* T_A = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CEO}	Collector-Emitter Voltage	45	V	
V _{CES}	Collector-Base Voltage	50	V	
V _{CBO}	Collector-Base Voltage	50	V	
V _{EBO}	Emitter-Base Voltage	5.0	V	
Ic	Collector Current - Continuous	200	mA	
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C	

^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

- 1) These ratings are based on a maximum junction temperature of 150 degrees C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

 3) All voltages (V) and currents (A) are negative polarity for PNP transistors.

Thermal Characteristics T_A = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units
		BC857S	
P_D	Total Device Dissipation	300	mW
	Derate above 25°C	2.4	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	415	°C/W

PNP Multi-Chip General Purpose Amplifier

(continued)

Electrical Characteristics

T_A = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
OFF CHAP	RACTERISTICS					
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	I _C = 10 mA, I _B = 0	45			V
V _{(BR)CES}	Collector-Base Breakdown Voltage	$I_C = 10 \mu A, I_E = 0$	50			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	$I_C = 10 \ \mu\text{A}, \ I_E = 0$	50			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	$I_E = 10 \ \mu A, \ I_C = 0$	5.0			V
I _{CBO}	Collector-Cutoff Current	V _{CB} = 30 V			15	nA
Í		$V_{CB} = 30 \text{ V}, T_A = 150^{\circ}\text{C}$			4.0	μΑ

ON CHARACTERISTICS

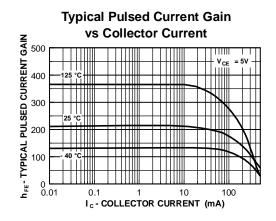
h _{FE}	DC Current Gain	$I_C = 2.0 \text{ mA}, V_{CE} = 5.0 \text{ V}$	125	630	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 10 mA, I _B = 0.5 mA		0.3	V
		$I_C = 100 \text{ mA}, I_B = 5.0 \text{ mA}$		0.65	V
V _{BE(on)}	Base-Emitter On Voltage	$I_C = 2.0 \text{ mA}, V_{CE} = 5.0 \text{ V}$	0.6	0.75	V
, ,		$I_C = 10 \text{ mA}, V_{CE} = 5.0 \text{ V}$		0.82	V

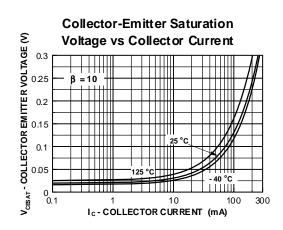
SMALL SIGNAL CHARACTERISTICS

f⊤	Current Gain - Bandwidth Product	$I_C = 10 \text{ mA}, V_{CE} = 5.0,$ f = 100 mHz	200	MHz
C _{obo}	Output Capacitance	V _{CB} = 10 V, f = 1.0 MHz	3.5	pF
NF	Noise Figure	$I_{C} = 0.2$ mA, $V_{CE} = 5.0$, $R_{S} = 2.0$ k Ω , $f = 1.0$ kHz, $BW = 200$ Hz	2.5	dB

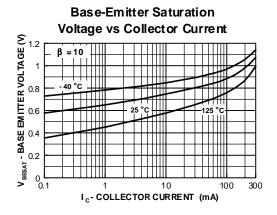
NOTE: All voltages (V) and currents (A) are negative polarity for PNP transistors.

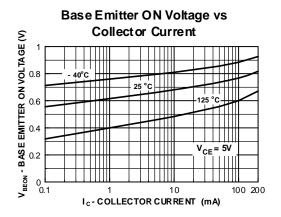
Typical Characteristics

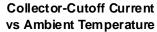


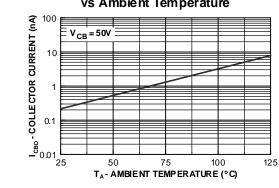


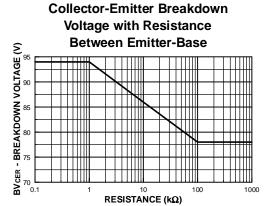
Typical Characteristics (continued)



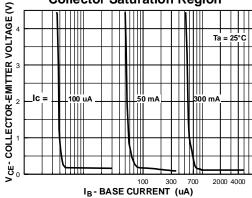




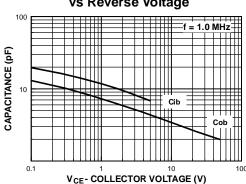




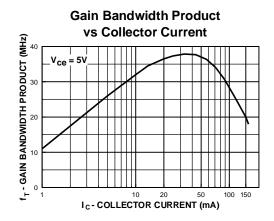
Collector Saturation Region

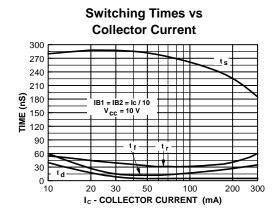


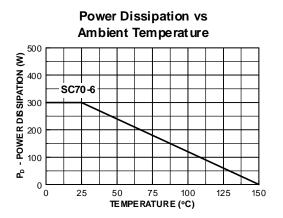
Input and Output Capacitance vs Reverse Voltage



Typical Characteristics (continued)







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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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